

BOARD OF TECHNICAL EDUCATION, DELHI

No. F 53(G)/2024-Exam./BTE

Dated : 27/08/24

Electronic Engineering (Digital Electronics & Microprocessor System Design)

Performance of the candidates (Compartment) of IV semester of three years Diploma Course in Engg. (in above mentioned branch) pertaining to examination held in JUN 2024 is communicated as under. The marks of candidates who have cleared full subjects out of an aggregate of 1200 are indicated against their roll numbers.

Roll No	Subjects in which declared passed / Marks Obtained
2021071009	ALL / 633
2021071015	ED470, ED471, ED473, ED480, ED481, ED482, ED483, ED484, ED485.
2021071018	ED470, ED471, ED473, ED480, ED481, ED482, ED483, ED484, ED485.
2021071020	ALL / 667
2021071031	ALL / 693


 Checked by


 Rechecked by

NOTE :

1. Eligible candidates who have not been successful in any of the subject or have not appeared in any subject are notified as failed
2. The performance of the candidates who have been placed under P.L.O. will be communicated separately, if necessary.

SUBJECT CODES :

Theories

ED470 Electronic Devices and Circuits-III
ED471 Introduction to Microprocessors
ED472 Electronics Instruments & Measurements
ED473 Personal Computer Organisation

Practicals

ED480 Electronic Devices and Circuits-III
ED481 Introduction to Microprocessors
ED482 Electronics Instruments & Measurements
ED483 Personal Computer Organisation
ED484 Electronic Design and Drawing
ED485 Minor Project

Forwarded to the Principal, Chhotu Ram Rural Institute of Technology, Kanjhawla, Delhi for information and necessary action.

S.P. Meher
(S. P. MEHER)
CONTROLLER

(COMPUTER CENTRE, BTEX)

